

An integrated framework for analysing, simulating and testing UML models

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Motivation

Context: UML as a notation for modelling object-oriented software systems

Goal: enable analyses supported by formal methods

Challenges:

- formal semantics (e.g., fUML, PSSM, PSCS)
- integration between structural and behavioural models (i.e., a unified semantics)



Contributions

Focus: behavioural (B) and structural (S) models

- B = state machine diagrams
- S = composite structure diagrams

Integration with the **NAT2TEST** strategy

Main **contributions:**

- Systematic and compositional process
- Validation of translation based on CSP
- Tool support along with the NAT2TEST tool
- Case studies:
 - dining philosophers
 - ring-buffer model

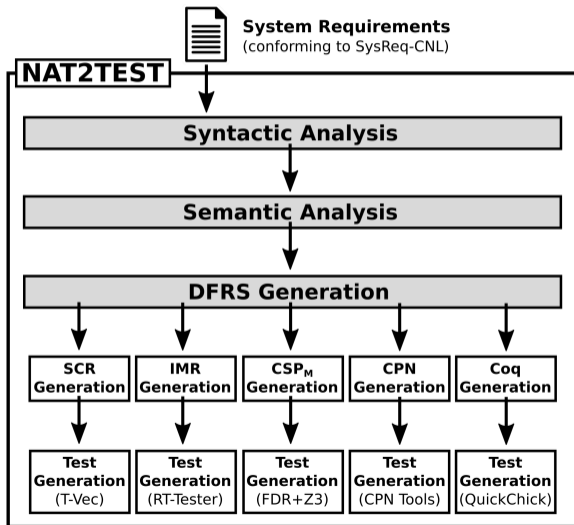


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NAT2TEST strategy



SysReq-CNL: guarded actions



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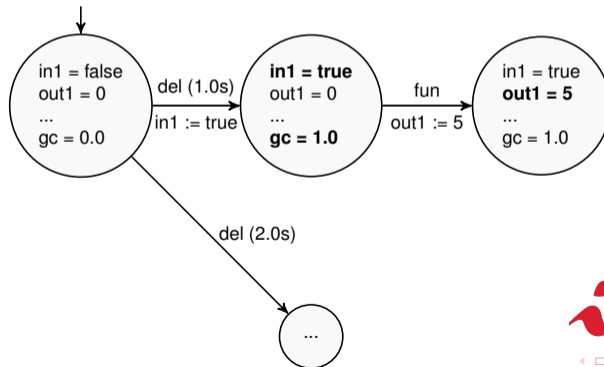
DFRSs: Data-flow Reactive Systems

s-DFRS: **symbolic** DFRS = $(I, O, T, gcvar, s_0, F)$

■ $F = \{f_1, f_2, \dots\}$, s.t. $f_1 = \{(static_guard_1, timed_guard_1) \mapsto \{action_1, action_2, \dots\}, \dots\}$

e-DFRS: **expanded** DFRS = $(I, O, T, gcvar, s_0, S, TR)$

■ del = delay transitions | fun = function transitions

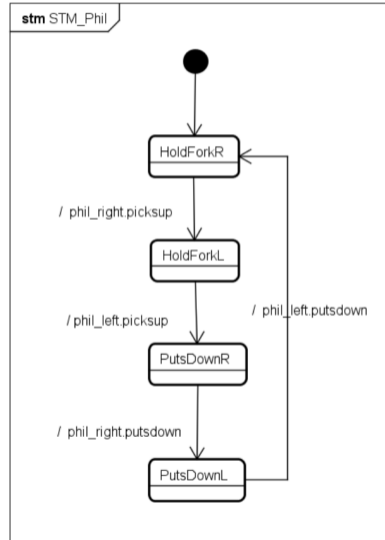
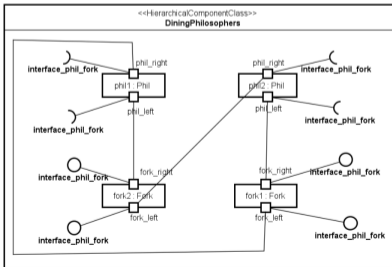
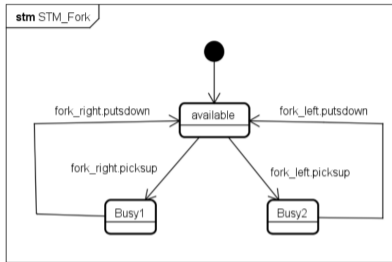


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Running example: dining philosophers



From UML diagrams to CNL requirements: overview

DFRSs: communication via **shared memory** (events represented using variables)

Translation follows **three** phases¹

- 1** SM: from transitions to sentences
(conditions \rightsquigarrow guards, effect \rightsquigarrow actions)
- 2** CS: sentences replicated based on the number of instances
(STM_Fork_state \rightsquigarrow STM_Fork_statefork1, ...)
- 3** CS: sentences updated according to connections
(sync.: one waits for the change, other performs the change)

Mapping rules: presented in the paper

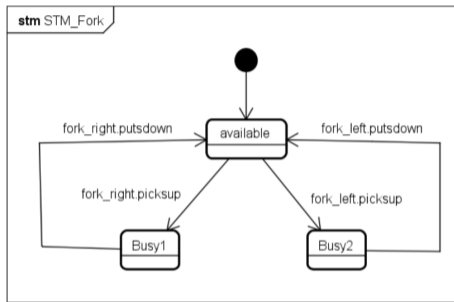
¹SM = state machines, CS = composite structure



From UML diagrams to CNL requirements: phase 1

Phase 1 – SM: from transitions to sentences

When the STM_Fork_state is available,
and the STM_Fork timer is greater than 0,
the STM_Fork_component shall:
assign true to the fork_right_picksup,
assign Busy1 to the STM_Fork_state,
reset the STM_Fork timer.



From UML diagrams to CNL requirements: phase 2

Phase 2 – CS: sentences replicated based on the number of instances

When the **STM_Fork_statefork1** is available, ...
the **STM_Fork_componentfork1** shall:
 assign true to the **fork_right_picksupfork1**,
 assign Busy1 to the **STM_Fork_statefork1**,

When the **STM_Fork_statefork2** is available, ...
the **STM_Fork_componentfork2** shall:
 assign true to the **fork_right_picksupfork2**,
 assign Busy1 to the **STM_Fork_statefork2**,



From UML diagrams to CNL requirements: phase 3

Phase 3 – CS: sentences updated according to connections

When the STM_Phil_statephil1 is HoldForkR, ...,
and the STM_Fork_statefork1 is available,
the STM_Phil_componentphil1 shall: **assign** true to the
phil_right_picksupphil1__fork_left_picksupfork1,

When the STM_Fork_statefork1 is available, ...,
and the phil_right_picksupphil1__fork_left_picksupfork1
becomes true,
the STM_Fork_componentfork1 shall: **assign** false to the
phil_right_picksupphil1__fork_left_picksupfork1,



From UML diagrams to CNL requirements: validation

Validation of translation based on a CSP semantics

- `S_NAT2TEST`: our semantics (derived by the NAT2TEST strategy)
- `S_UML`: obtained independently from the same diagrams [FLS+24]
- `S_Classical`: classical semantics written by Roscoe



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From UML diagrams to CNL requirements: validation

S_NAT2TEST

```
...
SPECIFICATION (memory) = FUN(...)
SPECIFICATION2 (memory) = ... delayTransition ...
SPECIFICATION3 (memory) = INPUTS (memory)
SPECIFICATION4 (memory) = SPECIFICATION (memory)

SYSTEM = SPECIFICATION (seq(initialBinding))
SYSTEM' = SYSTEM [[ ... <- ... ]]
S' = SYSTEM' \ { | ... | }

transparent sbisim, diamond
sbdia(P) = sbisim(diamond(P))
S'' = (sbdia(S'))

S_NAT2TEST = S''
S_NAT2TEST' = S_NAT2TEST \ { ... }
```

S_Classical

```
...
PHILS = ||| i:PHILNAMES @ PHIL(i)
FORKS = ||| i:FORKNAMES @ FORK(i)
PHILS_SYSTEM = PHILS [| {picks, putsdown}|] FORKS

S_Classical = PHILS_SYSTEM
S_Classical' = S_Classical [[ ... <- ...]] \ { ... }
```

S_UML

```
...
processcomp =
(inter_inter_inter_fork2_phil2_phil1_fork1)
[| ... |] BFIO_INIT(fork_left.2,phil_right.1)

processcomp_com = processcomp
[| ... |] BFIO_INIT(fork_right.2,phil_left.2)

processcomp_com_com = processcomp_com
[| ... |] BFIO_INIT(fork_right.1,phil_left.1)

processcomp_com_com_com = processcomp_com_com
[| ... |] BFIO_INIT(fork_left.1,phil_right.2)

S_UML = processcomp_com_com_com
S_UML' = S_UML [[ ... <- ...]] \ { ... }
```



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From UML diagrams to CNL requirements: validation

We prove (via FDR) that:

```
assert S_NAT2TEST' [T= S_UML'  
assert S_UML' [T= S_NAT2TEST'
```

```
assert S_NAT2TEST' [T= S_Classical'  
assert S_Classical' [T= S_NAT2TEST'
```

All models have precisely the **same trace semantics**



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Tool support

JavaScript code: obtain textual representation of AstahUML models

```
machine STM_Fork  
state Available ...  
transition Available fork_right.picksup Busy1 ...
```

Python scripts: implement the mapping rules in a **very direct way**

- Uses text templates

```
When the {1}_state is {2},  
and the {3} timer is greater than 0,  
the {4}_component shall:  
assign true to the {5},  
assign {6} to the {7}_state,  
reset the {8} timer.
```



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Tool support provided by the NAT2TEST strategy

The screenshot displays the NAT2TEST application window, titled "NAT2TEST - From Natural Language Requirements to Test Cases". The interface includes a menu bar (File, Project, Window, Help), a toolbar, and a navigation pane on the left. The main area shows a state machine diagram with nodes 0 through 14 and transitions. The diagram includes delay transitions (e.g., delay := 1) and function transitions (e.g., FUN: the_phil_right_picksupfork2 := true; the_stm_phil_timer := 1;). The navigation pane lists requirements (REQ001 to REQ016) and other system components. The console at the bottom shows the state of 9 variables (15 instances).

Enabled Transitions

Source	Type	Transition
9	Delay	DEL: +7 time units; ? := ?; ...

History

Source	Type	Transition	Target
0	Delay	DEL: +1 time units;	1
1	Function	FUN: the_phil_right_picksup	2
2	Function	FUN: the_phil_right_picksup	3
3	Delay	DEL: +1 time units;	4
4	Function	FUN: the_phil_left_picksup	5
5	Function	FUN: the_phil_left_picksup	6
6	Function	FUN: the_phil_right_picksup	7
7	Function	FUN: the_phil_right_picksup	8

Console

Kind	Name	Type	Value
OUTPUT	the_phil_right_outdownphil1_fork_left_outdownfork1	BOOLEAN	false
OUTPUT	the_stm_phil_statephi2	INTEGER	0
OUTPUT	the_stm_phil_statephi1	INTEGER	0
OUTPUT	the_phil_right_picksupphi1_fork_left_picksupfork1	BOOLEAN	false
OUTPUT	the_phil_right_picksupphi1_fork_left_picksupfork2	BOOLEAN	false
OUTPUT	the_phil_left_picksupphi2_fork_right_picksupfork1	BOOLEAN	false
OUTPUT	the_phil_left_outdownphi1_fork_right_outdownfork2	BOOLEAN	false
OUTPUT	the_stm_fork_statefork2	INTEGER	2
OUTPUT	the_stm_fork_statefork1	INTEGER	2

Tool support provided by the NAT2TEST strategy



Related work

	B. diagr.	S. diagr.	Simul.	Testing	Verif.	Code gen.
CA21	SM	BD, IB	●	○	●	○
LMC ⁺ 17	Ac, SM, Sq	BD, IB	○	○	●	○
HMG ⁺ 23	Ac, SM, Sq	BD, IB	●	●	●	●
EMM24	Ac, SM, Sq	–	●	◐	○	○
FLS ⁺ 24	SM	CI, CS	●	○	●	○
Ours	SM	CS	●	●	●	○

CA21: *Direct Model-checking of SysML Models.*

LMC⁺17: *An integrated semantics for reasoning about SysML design models using refinement.*

HMG⁺23: *Pragmatic verification and validation of industrial executable SysML models.*

EMM24: *To Do or Not to Do: Semantics and Patterns for Do Activities in UML PSSM State Machines.*

FLS⁺24: *A formal component model for UML based on CSP aiming at compositional verification.*

Conclusion

Analysing, simulating, and testing UML models

- Combination of behavioural + structural diagrams
- Enabled by the NAT2TEST strategy

Future work:

- Complex constructs
- More UML diagrams
- Additional case studies
- Further explore the NAT2TEST strategy



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